

Sheet 1 of 1

Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-202002	Application No. 10/828,913
	Applicant Bradley C. Aldrich, et al.		
	Filing Date April 20, 2004	Group Art Unit 2183 2193	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
[Signature]	AA	4,748,585	05/1988	Chiarulli, et al.			
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Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
[Signature]	AL	0 654 733	05/1995	Europe				
	AM	WO 98/32071	07/1998	WIPO				
	AN	WO 99/31601	06/1999	WIPO				
	AO							
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Other Documents (include Author, Title, Date, and Place of Publication)		
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[Signature]	AQ	Bermak, et al., "High-density 16/8/4-bit configurable multiplier", <i>IEE Proc.-Circuits Devices Syst.</i> , 144(5):272-276, (1997).
[Signature]	AR	Kim, et al., "4-way Superscalar DSP Processor for Audio Codec Applications", Proceedings of the 1998 IEEE International Conference on Acoustics, Speech and Signal Processing, ICASSP, '98, Seattle, WA, USA, May 12-15, 1998, IEEE International Conference on Acoustics, Speech and Signal Processing, New York, NY, Vol. 5, Conf. 23, May 12, 1998, pp. 3117-3120.
[Signature]	AS	Singh, et al., "GaAs Low-Power Integrated Circuits for a High-Speed Digital Signal Processor", <i>IEEE Transactions on Electron Devices</i> , 36(2):240-249, (1989).

Examiner Signature <u>D.H. Malczuk</u>	Date Considered <u>6/24/05</u>
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	